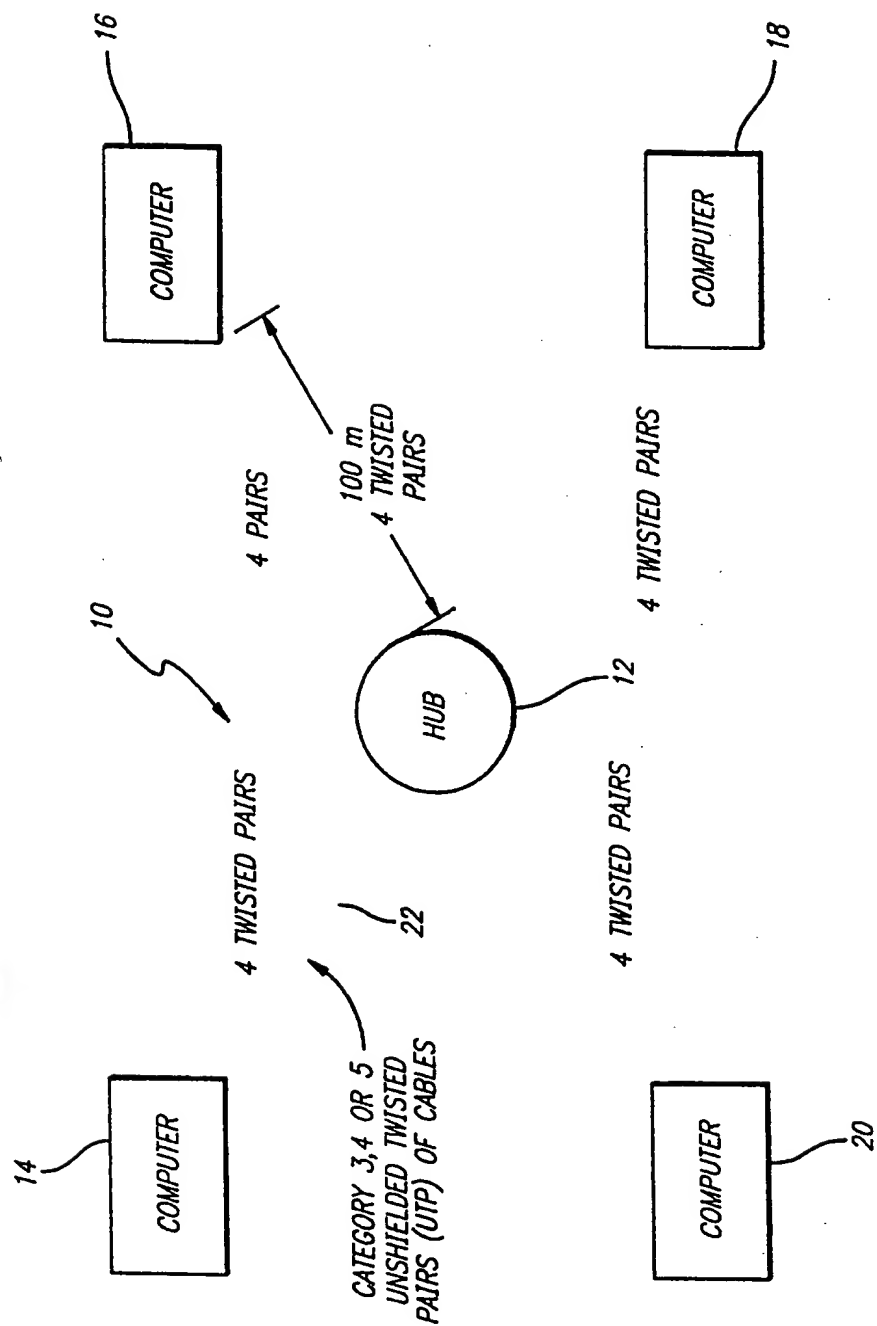
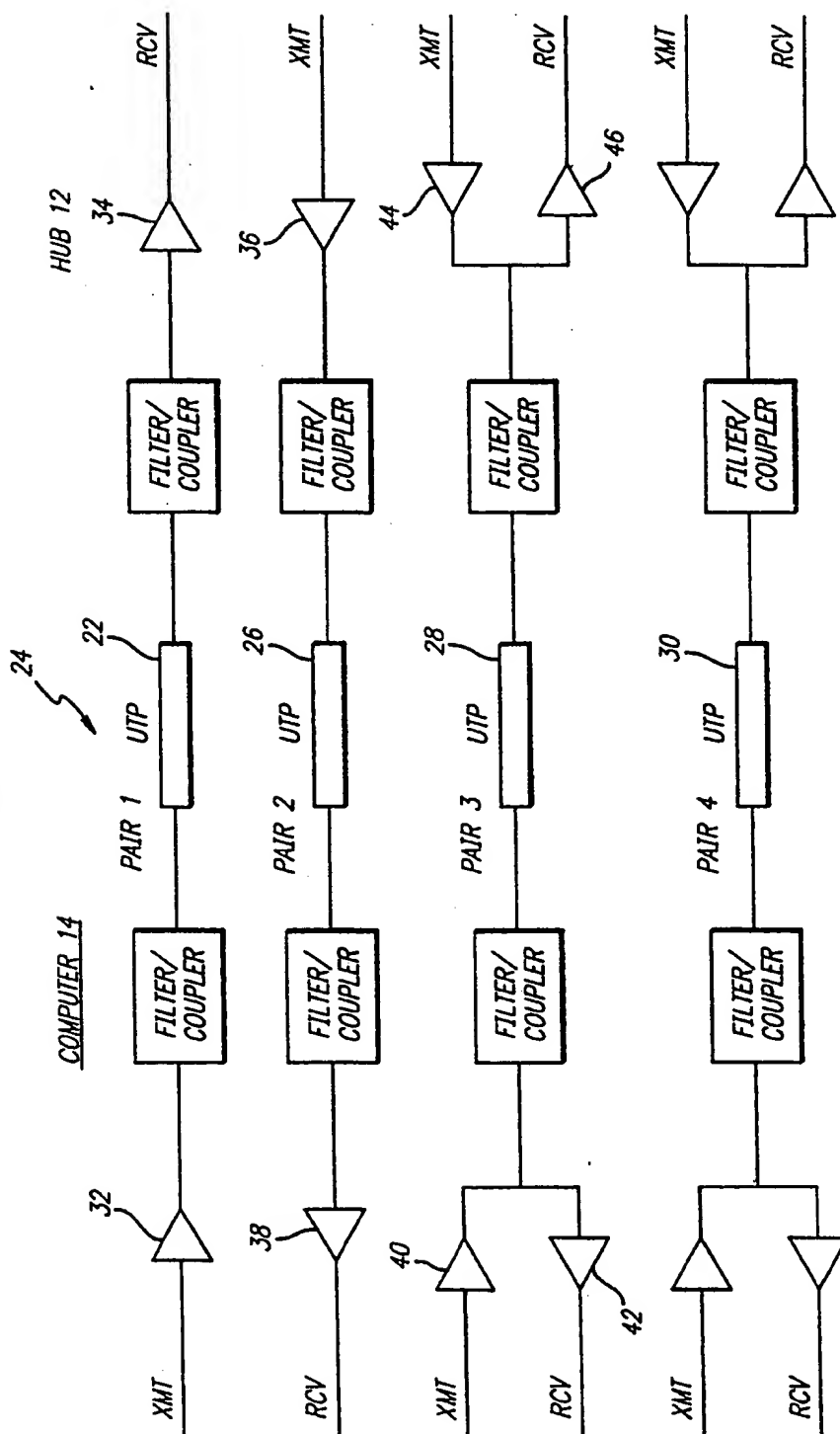


FIG. 1 *PRIOR ART*

100 Mb/s ETHERNET LOCAL AREA NETWORK (LAN)



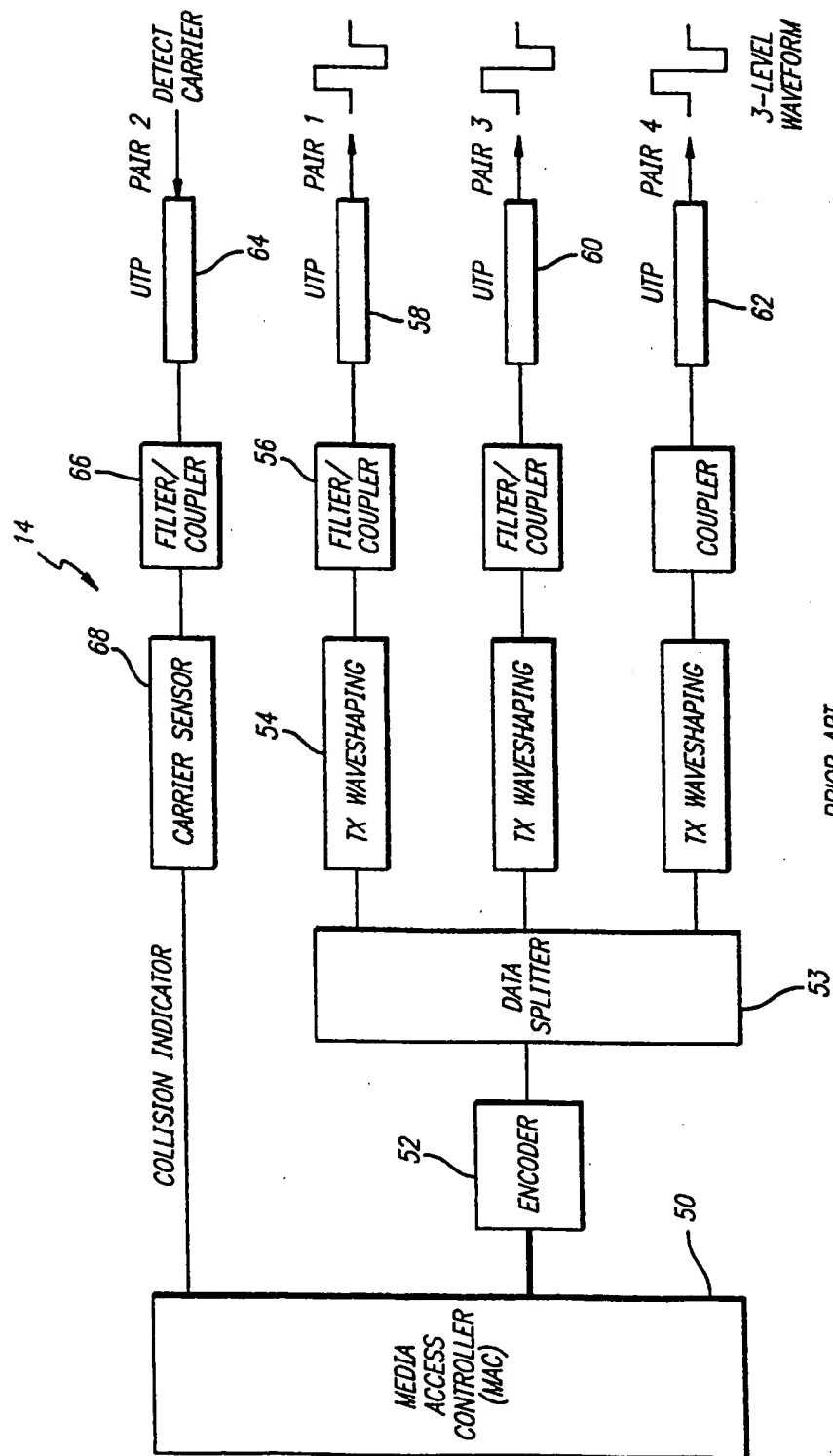
OVERVIEW



PRIOR ART

FIG. 2

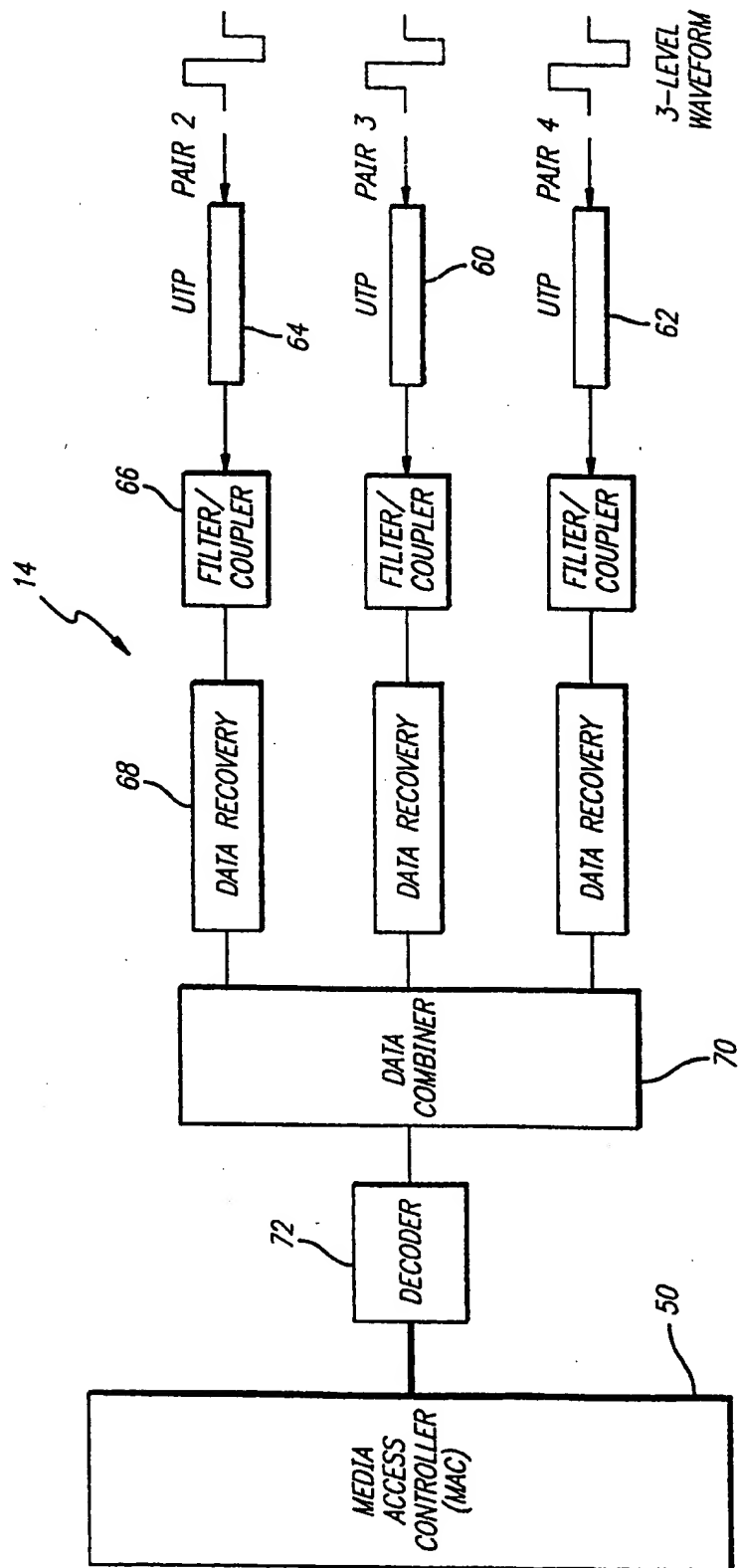
TRANSMITTER BLOCK DIAGRAM FOR COMPUTER 14



PRIOR ART

FIG. 3

RECEIVER BLOCK DIAGRAM FOR COMPUTER 14



PRIOR ART

FIG. 4

FIG. 5

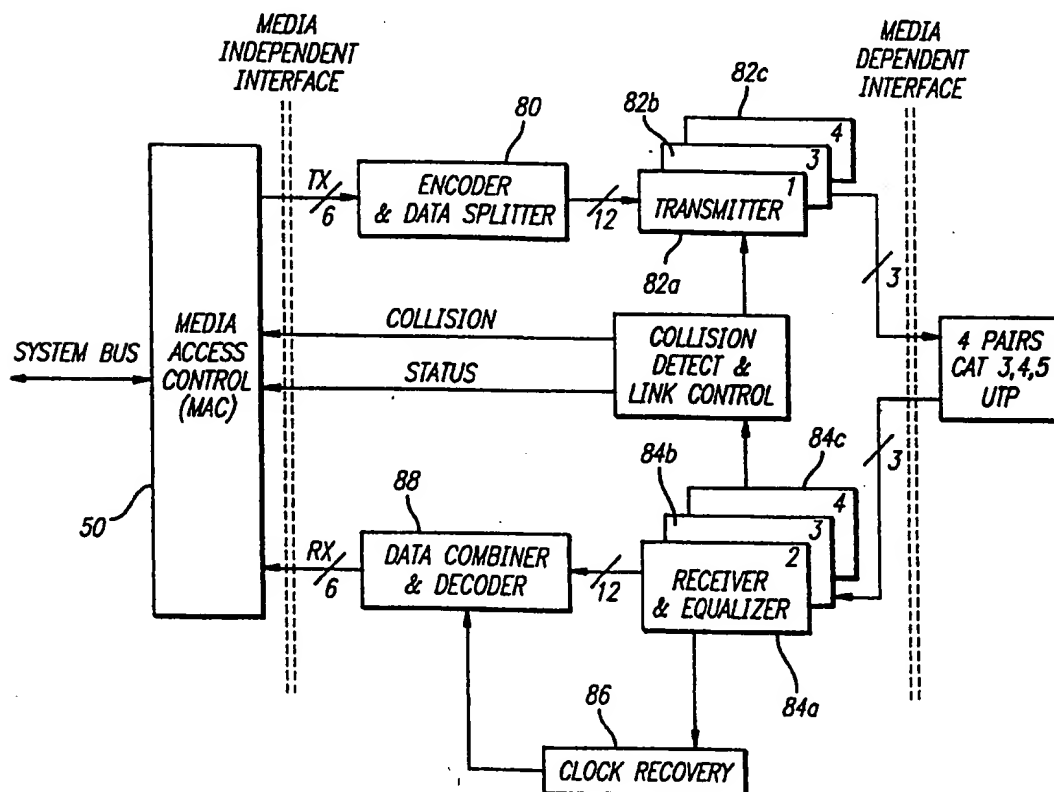
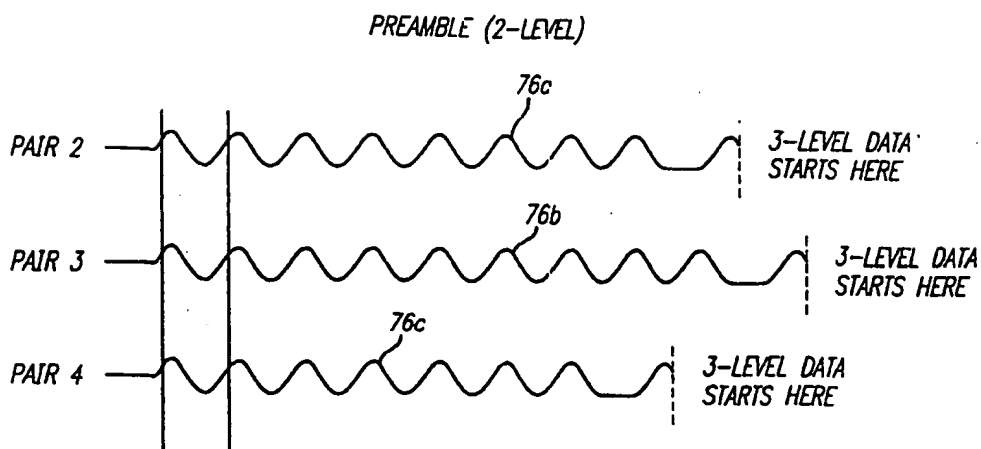


FIG. 6

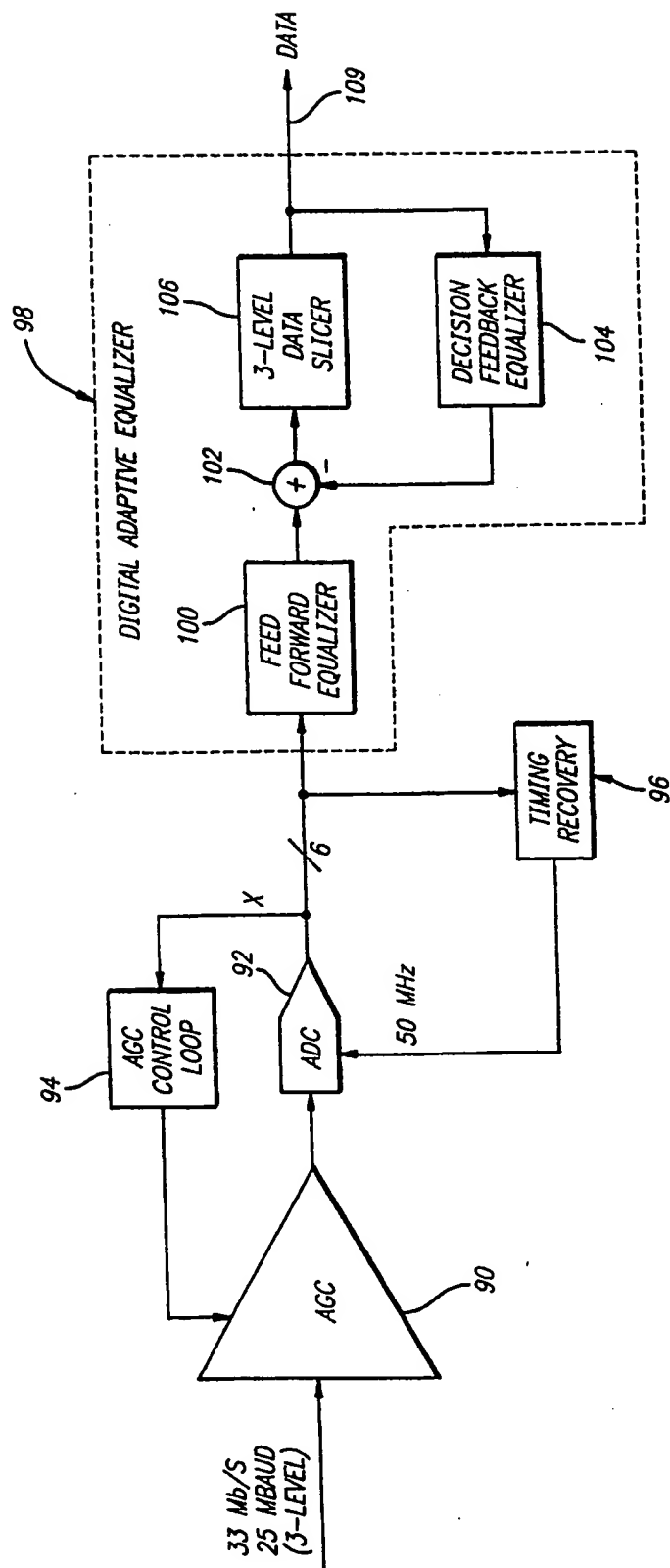


FIG. 7

00T220" 6T602960

FIG. 8

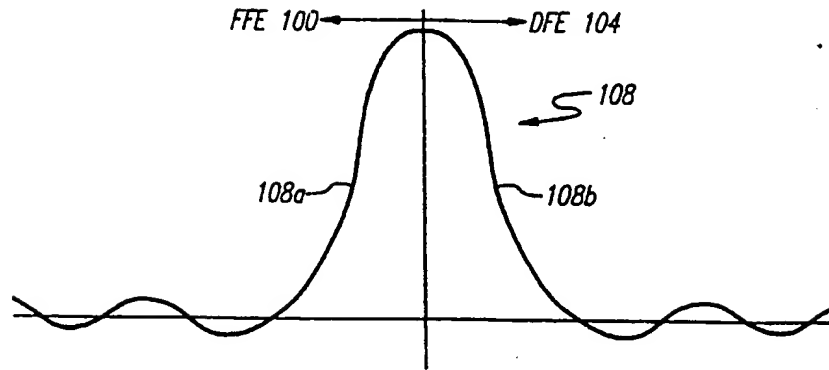


FIG. 12a

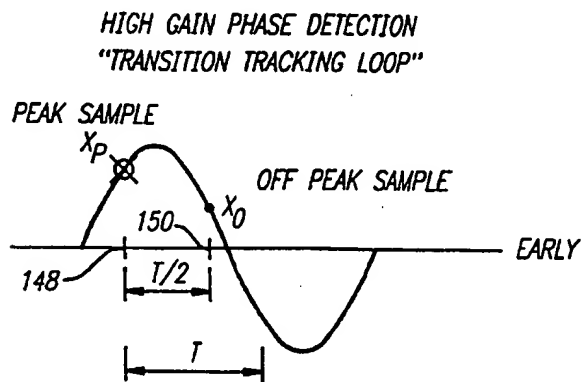


FIG. 12b

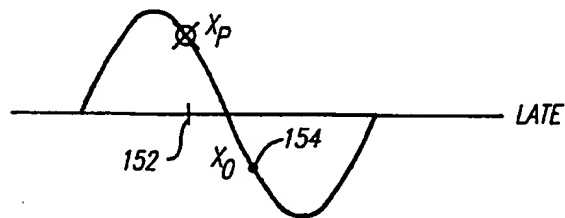
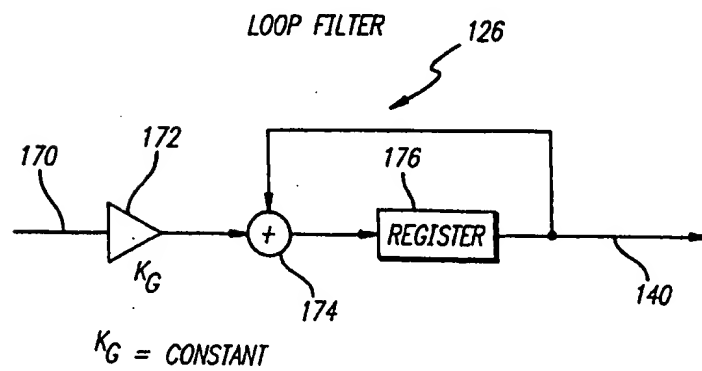


FIG. 15



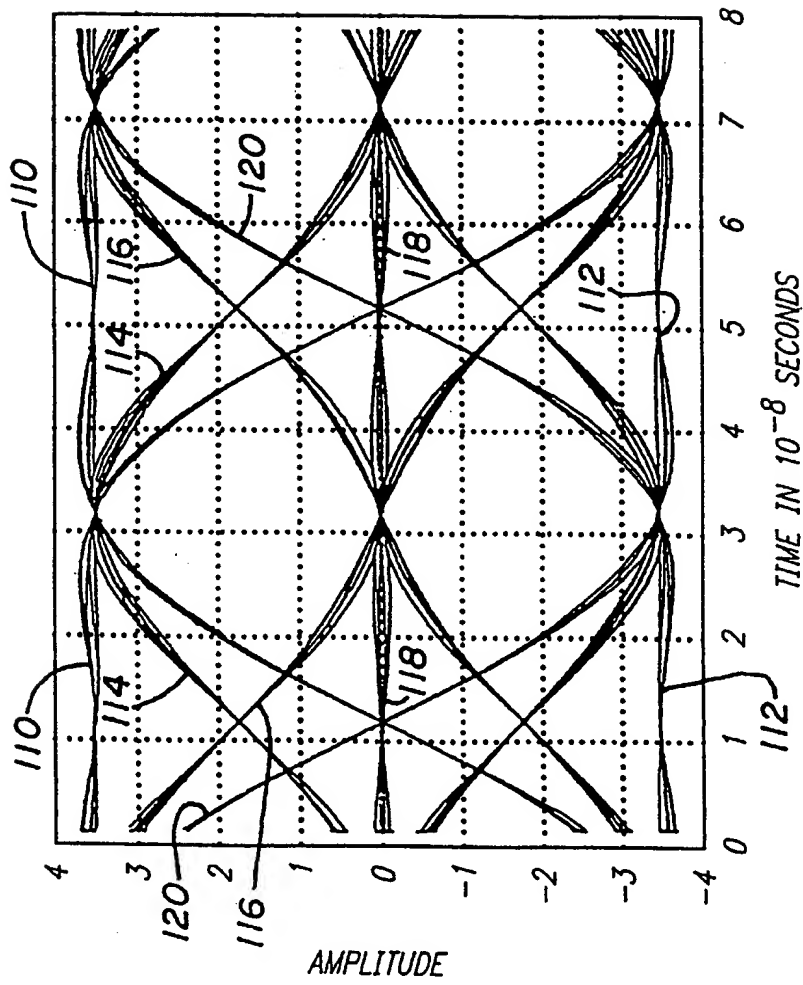


FIG. 9

RECEIVED SIGNAL AFTER 30m
ALONG CATEGORY3 CABLE

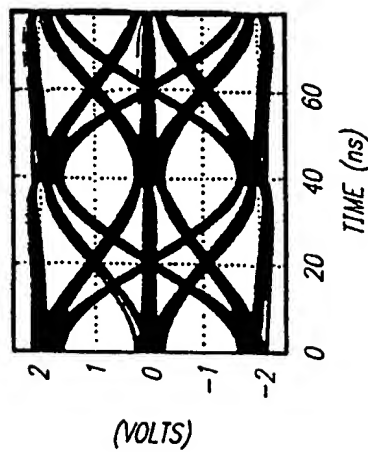


FIG. 10a

RECEIVED SIGNAL AFTER 60m
ALONG CATEGORY3 CABLE

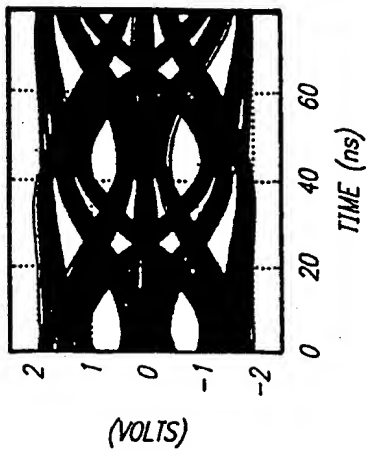


FIG. 10b

RECEIVED SIGNAL AFTER 100m
ALONG CATEGORY3 CABLE

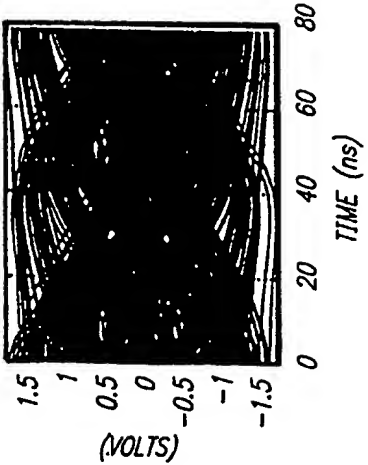
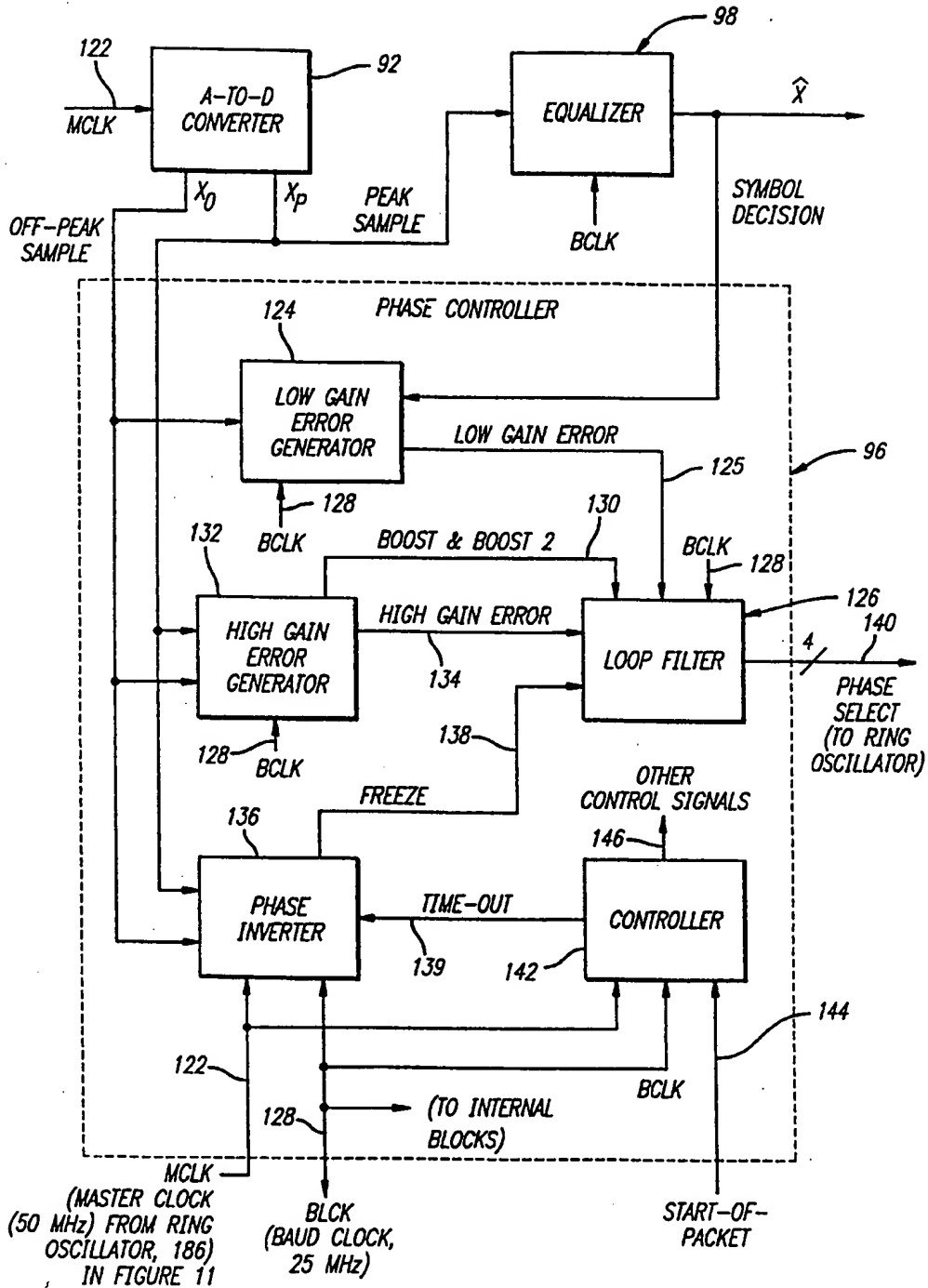


FIG. 10c

FIG. 11



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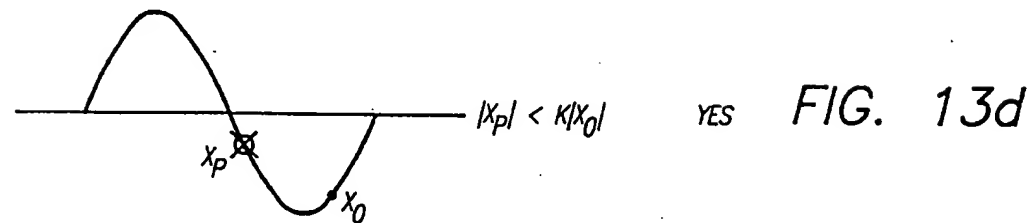
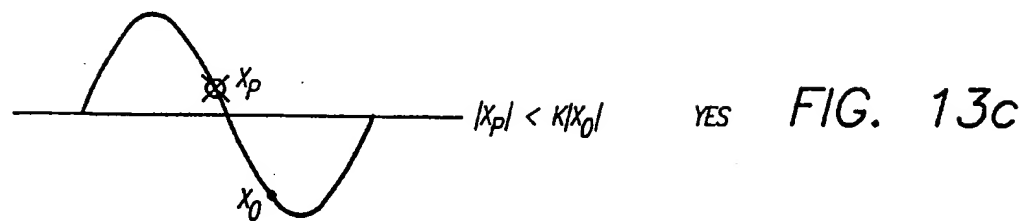
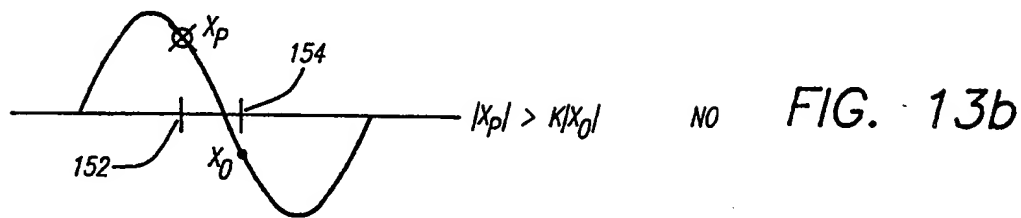
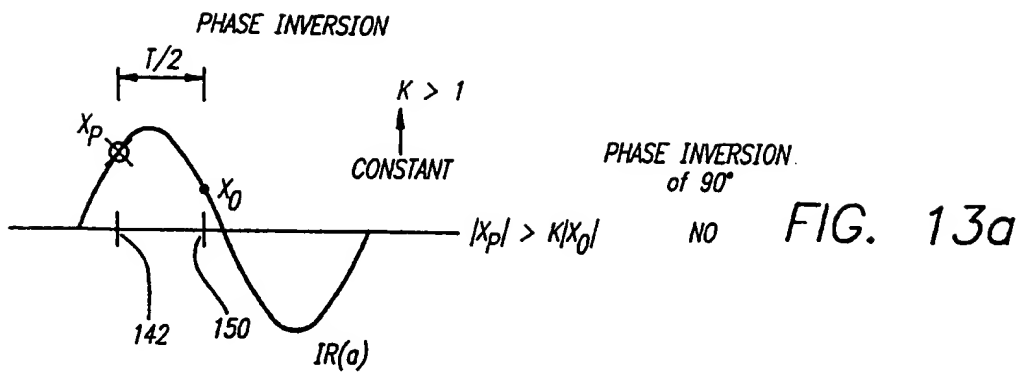


FIG. 14

LOW GAIN PHASE DETECTION
"TRANSITION TRACKING LOOP"

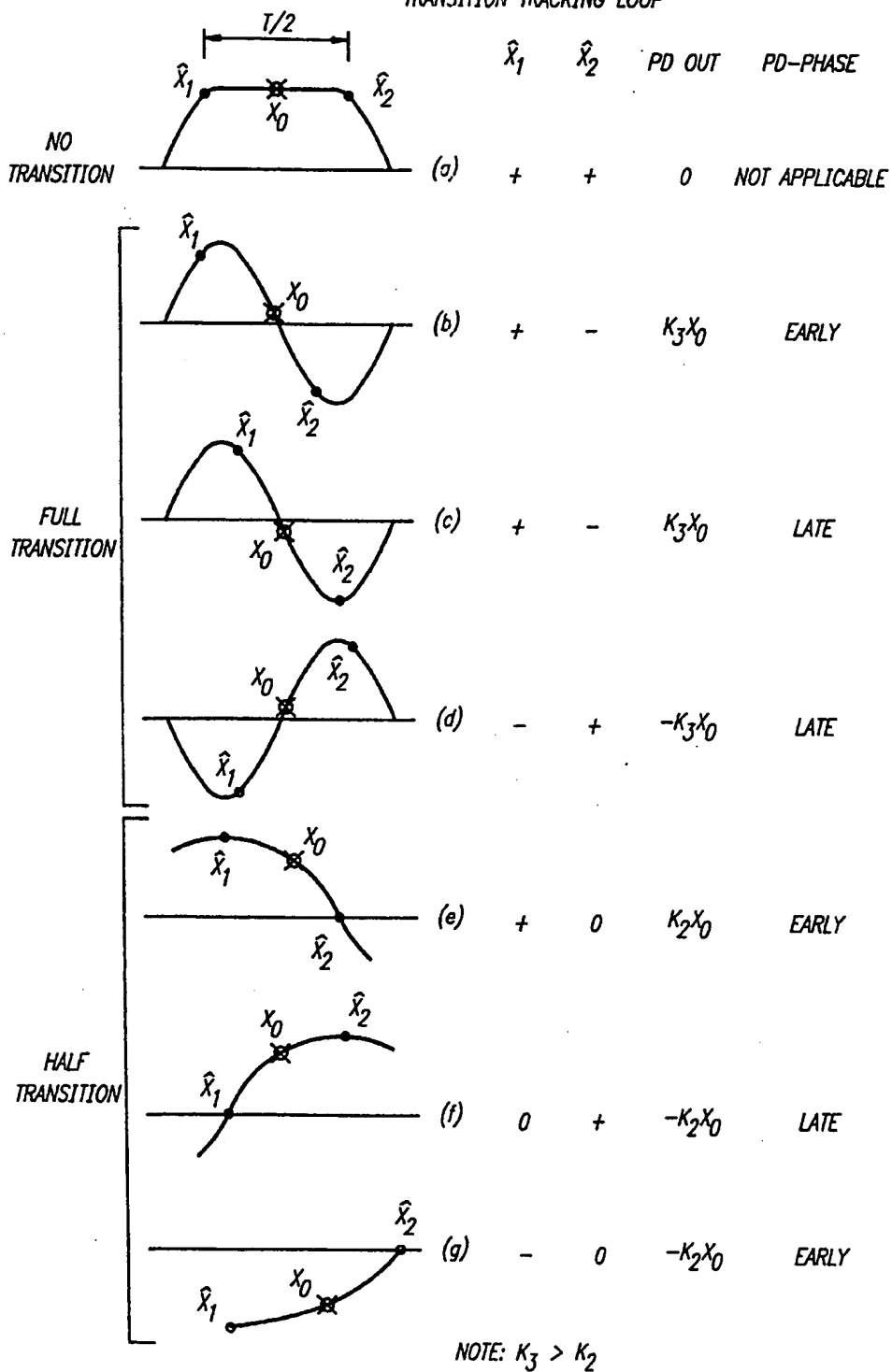
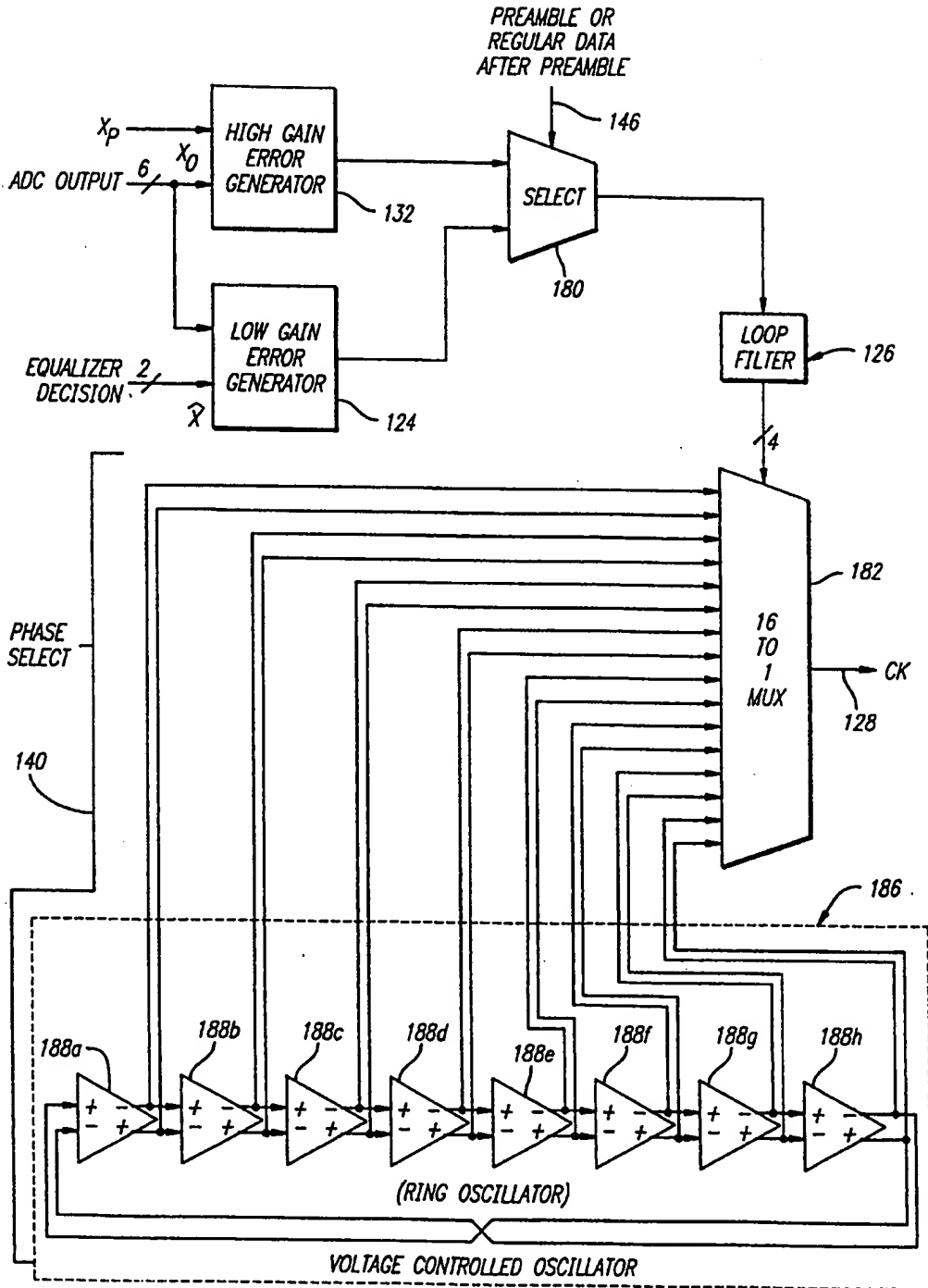


FIG. 16



001220" 6T602950